

JC-535 JCEG U.S. PTO
10/036789



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U.S. UTILITY Patent Application

**PATENT NUMBER and
ISSUE DATE**

APPL. NO.	FILED DATE	CLASS	SUBCLASS	GAU	EXAMINER
10056789	12/21/2001	712	2117	2456	Eng

****APPLICANTS:** Pechanek Gerald; Kurak Charles;

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A CON OF 09/323,609 06/01/1999 PAT 6,338,129
WHICH IS A DIV OF 08/885,310 06/30/1997 PAT 6,023,753

FOREIGN APPLICATIONS VERIFIED:

PG. PUB. DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	800.0119
Verified and Acknowledged Examiner's initials	
TITLE : Manifold array processor	

U.S. DEPT. OF COMM./PAT. & TM-PTO-435L (Rev. 12-91)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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☒ **TERMINAL
DISCLAIMER**
6,338,129

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(Attached in pocket on right inside flap)